

IN THE SPECIFICATION

On page 1 of the specification, please insert the following text starting on line 9 between the "RELATED APPLICATIONS" and "TECHNICAL FILED OF THE INVENTION" paragraphs as follows:

--GOVERNMENT RIGHTS

This invention was made with government support under Grant Nos. CCR-9971192 and CCR-9406946 awarded by the National Science Foundation (NSF). Accordingly, the government has certain rights in this invention.--

For the Examiner's convenience, a revised Page 1 of the specification with the above-referenced changes has been enclosed.

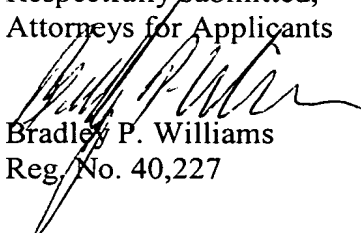
CONCLUSION

Early and favorable acceptance of this preliminary amendment is respectfully requested.

Applicants believe that no fee is due. The Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 02-0384 of Baker Botts L.L.P.

If there are matters that can be discussed by telephone to further the prosecution of this application, Applicants respectfully request that the Examiner call the attorney at the number listed below.

Respectfully submitted,  
Attorneys for Applicants

  
Bradley P. Williams  
Reg. No. 40,227

Correspondence Address:

Bradley P. Williams, Esq.  
Baker Botts L.L.P.  
2001 Ross Avenue, Suite 600  
Dallas, Texas 75201-2980  
Tel: 214.953.6447  
Fax: 214.661.4447  
brad.williams@bakerbotts.com

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ATTORNEY'S DOCKET NO.:  
017575.0551

PATENT APPLICATION

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COPY OF PAPERS  
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SYSTEM AND METHOD FOR DETECTING QUIESCENT  
CURRENT IN AN INTEGRATED CIRCUIT

RELATED APPLICATIONS

This application claims the priority under 35 U.S.C.  
§119 of provisional application serial number 60/250,735  
5 entitled "Current Sensor System and Method for Measuring  
Integrated Circuit Current (IDDQ), by Duncan M. Walker,  
et al., filed December 1, 2001.

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TECHNICAL FIELD OF THE INVENTION

This invention relates in general to current  
detection, and more particularly to a system and method  
for detecting quiescent current in an integrated circuit.